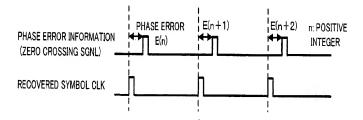
FIG. 2

PRIOR ART

PR UW DATA







1/3

FIG. 4 PRIOR ART

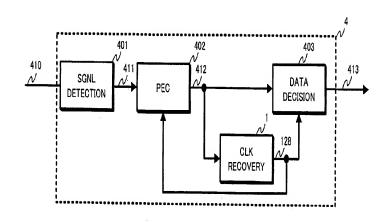


FIG. 5 PRIOR ART

BIT Xn Xn+1	PHASE TRANSITION
11	-3π/4
0 1	3π/4
0.0	π/4
1 0	-π/4

FIG. 6

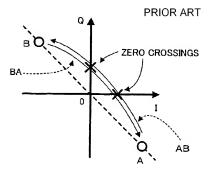
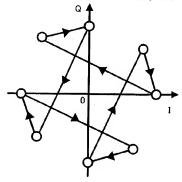


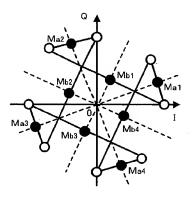
FIG. 7 PRIOR ART

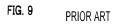


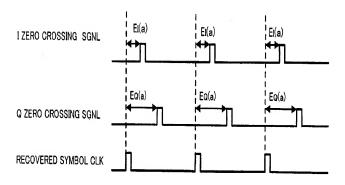
REPLACEMENT SHEET

FIG. 8

PRIOR ART









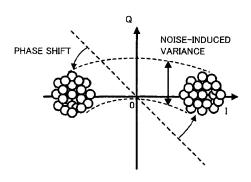


FIG. 11 PRIOR ART

